10/645,040

| EAST History |        | 12/3/04  | 2:02 PM                         |
|--------------|--------|--|---------------------------------|
| IS&R         | 3      | (("5162822") or ("5646584") or ("6377137")).PN.  | USPAT                           |
| IS&R         | 8      | (("4,999,528") or ("5,418,407") or ("5,510,732") or ("5,754,070") or ("5,793,227") or ("ô,867,695") or ("5,999,029") or ("6,072,346") or ("6,384.619") or ("6,429,692")).PN. | USPAT                           |
| IS&R         | 3672   | ((326/93-98,56-58,105) or (327/198,142,145)).CCLS.   | USPAT                           |
| IS&R         | · 3891 | ((326/93-98,56-58,105) or (327/198,142,145)).CCLS.   | US-PGPUB;<br>USPAT              |
| BRS          | 649    | synchronizer adj circuit   | US-PGPUB;<br>USPAT; EPO;<br>JPO |
| BRS          | 12243  | synchroniz\$ adj circuit   | US-PGPUB;<br>USPAT; EPO;<br>JPO |
| BRS          | 108    | S179 and S182  | US-PGPUB;<br>USPAT; EPO;<br>JPO |
| BRS          | 45     | S180 and (selector adj circuit)  | USPAT                           |
| BRS          | 405    | S180 and multiplexer   | USPAT                           |
| BRS          | 148    | S185 and synchroniz\$  | USPAT                           |
| BRS          | 7      | S179 and (clock adj selector)  | USPAT                           |
| BRS          | 6      | ("5793227").URPN.  | USPAT                           |
| BRS          | 7      | ("5652536"   "6107841"   "6323715"   "6411135"   "6453425"   "6559679"   "6563349").PN.  | US-PGPUB;<br>USPAT;<br>USOCR    |
| BRS          | 16     | ("5654988").URPN.  | USPAT                           |
| BRS          | 3      | ("5604452"   "5652536"   "6240695").PN.  | US-PGPUB;<br>USPAT;<br>USOCR    |
| IS&R         | 3672   | ((326/93-98,56-58,105) or (327/198,142,145)).CCLS.   | USPAT                           |
| BRS          | 204    | S192 and (switching adj circuit)   | USPAT                           |
| BRS          | 15     | S193 and multiplexer   | USPAT                           |

| EAST History |     | 12/3/04                            | 2:02 PM                         |
|--------------|-----|------------------------------------|---------------------------------|
| BRS          | 649 | synchronizer adj circuit           | US-PGPUB;<br>USPAT; EPO;<br>JPO |
| BRS          | 87  | synchronizer adj logic             | US-PGPUB;<br>USPAT; EPO;<br>JPO |
| BRS          | 724 | S195 or S196                       | US-PGPUB;<br>USPAT; EPO;<br>JPO |
| BRS          | 360 | S197 and (multiplexer or selector) | US-PGPUB;<br>USPAT; EPO;<br>JPO |
| BRS          | 22  | S198 and (switching adj circuit)   | US-PGPUB;<br>USPAT; EPO;<br>JPO |